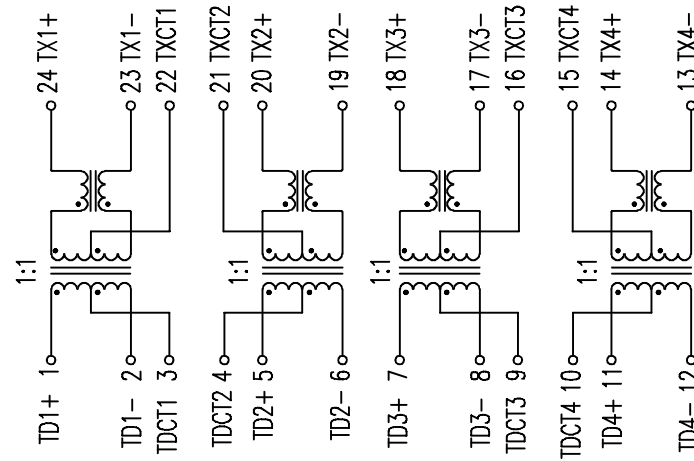


SUGGESTED PCB PAD LAYOUT



ELECTRICAL CHARACTERISTICS :

1. INSULATION HI-POT : 1500 Vrms/50Hz, 60Sec.
2. TURN RATIO (±5%) : 1CT : 1CT
3. OCL (@100KHz, 0.1Vrms) : 350uH MIN.
4. CROSS TALK (dB MIN.) : 30MHz ; -45, 60MHz ; -40, 80MHz ; -35
5. INSERTION LOSS (1MHz-100MHz) : -1.0dB MAX.
6. RETURN LOSS (dB MIN. @100 Ω) :

1-30MHz	40MHz	50MHz	60-80MHz	100MHz
-18	-16	-16	-12	-10
7. DIFFERENTIAL TO COMMON MODE REJECTION (dB MIN.) :

30MHz	60MHz	100MHz
-43	-37	-33
8. LEAKAGE INDUCTANCE (@100KHz, 0.1Vrms) : 0.4uH MAX
9. Cw/w (@100KHz, 0.1Vrms) : 28pF MAX
10. DCR : 1.2Ω MAX
11. RoHS COMPLIANT

UNLESS OTHERWISE SPECIFIED , TOLERANCES XX.X0 ±0.25mm
0.XX ±0.05mm

						AOEM.	TITLE 100/1000 BASE-T TRANSFORMER	
							DWG. NO. ATPL-437R	
RELEASE						UNITS:	SAFETY	
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE	P/N:	SHEET 1 OF 1 DRAW
REVISIONS								